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APPLICANTS

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** CONTINUING DATA *****

This application is a CON of 09/714,127 11/17/2000 PAT 6,635,918
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** FOREIGN APPLICATIONS *****

JAPAN 8-3648 01/12/1996

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	58	1	1
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>				

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as per amdt
filed on 3/7/05

TITLE *including memory cell section having capacitor over bitline structure and with the memory and peripheral sections having contact plug structures containing a barrier film and effecting electrical contact with contacts of both memory and peripheral sections*
Semiconductor integrated circuit device and method for manufacturing the same

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